INDRAPRASTHA INSTITUTE *of* INFORMATION TECHNOLOGY **DELHI**

(A State University established by Govt. of NCT of Delhi)

Date: 20.06.2025

Subject: Applications are invited for a research associate position in the area of communication with expertise of Field-Programmable Gate Arrays (FPGA) Design.

Post	Research Assistant, 1 position available.
Reporting to	Prof. Vivek Ashok Bohara (Professor, Department of Electronics and Communication, IIIT-Delhi)
Monthly Remunerations	* Rs 15k-25k (all inclusive). * Depending on the experience.
Essential Qualifications	The candidate should possess: 1) B.E./B.Tech in Electronics and Communication Engineering or a related field. 2) A strong background in optical and wireless communication with hands-on experience in FPGAs. 3) USRP and MATLAB software knowledge will be an added advantage.
Responsibilities	The selected candidate will work on integrating MATLAB/Python FPGA to design a wireless communication links.
Duration at IIIT-Delhi	6 Months (Possibility of 6 months extension on the basis of performance).
Last date for Application	The posting will remain open until a suitable candidate is found.
Application Process:	Interested candidates are invited to apply through this link: Link The shortlisted candidate will be informed via email and followed up with the interview round by first week of July. IIITD norms will be followed in the hiring process.